

Application No.: 09/518,117

Docket No.: JCLA5710

In the Specification:

Please replace the paragraph beginning on page 4, line 6 and the paragraph on page 4, beginning at line 8 as follows:

Fig. 1 is a timing diagram showing the shift gate clock pulse and the motor pulse provided by the driving system of a conventional scanner;

Fig. 2 is a timing diagram showing the shift gate clock pulse and the motor pulse provided by the driving system of a scanner according to this invention; and

Fig. 3 is a schematic diagram of the driving system control according to the present invention.

Please insert the following paragraph between the first and second paragraphs on page 5 as follows:

Fig. 3 is a schematic diagram of a scanner showing the driving system control 10 sending the motor pulse (MTP) 50 to the motor 30 and shift gate clock pulse (SH) 40 to the CCD 20 according to this invention.